

Figure 1

Dynamic Gain Equalization Filter Control System

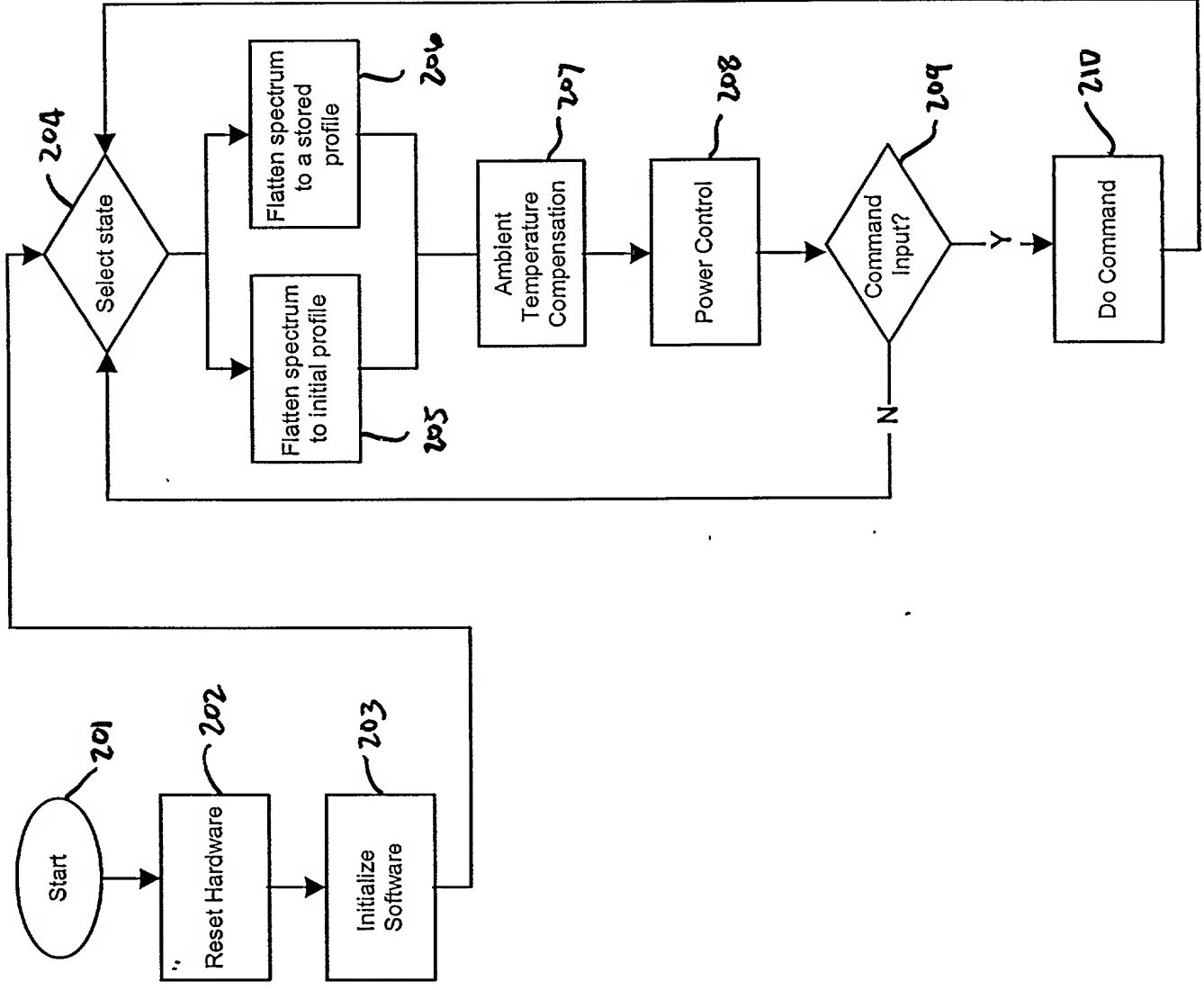


Figure 2

Dynamic Gain Equalization Filter State Diagram

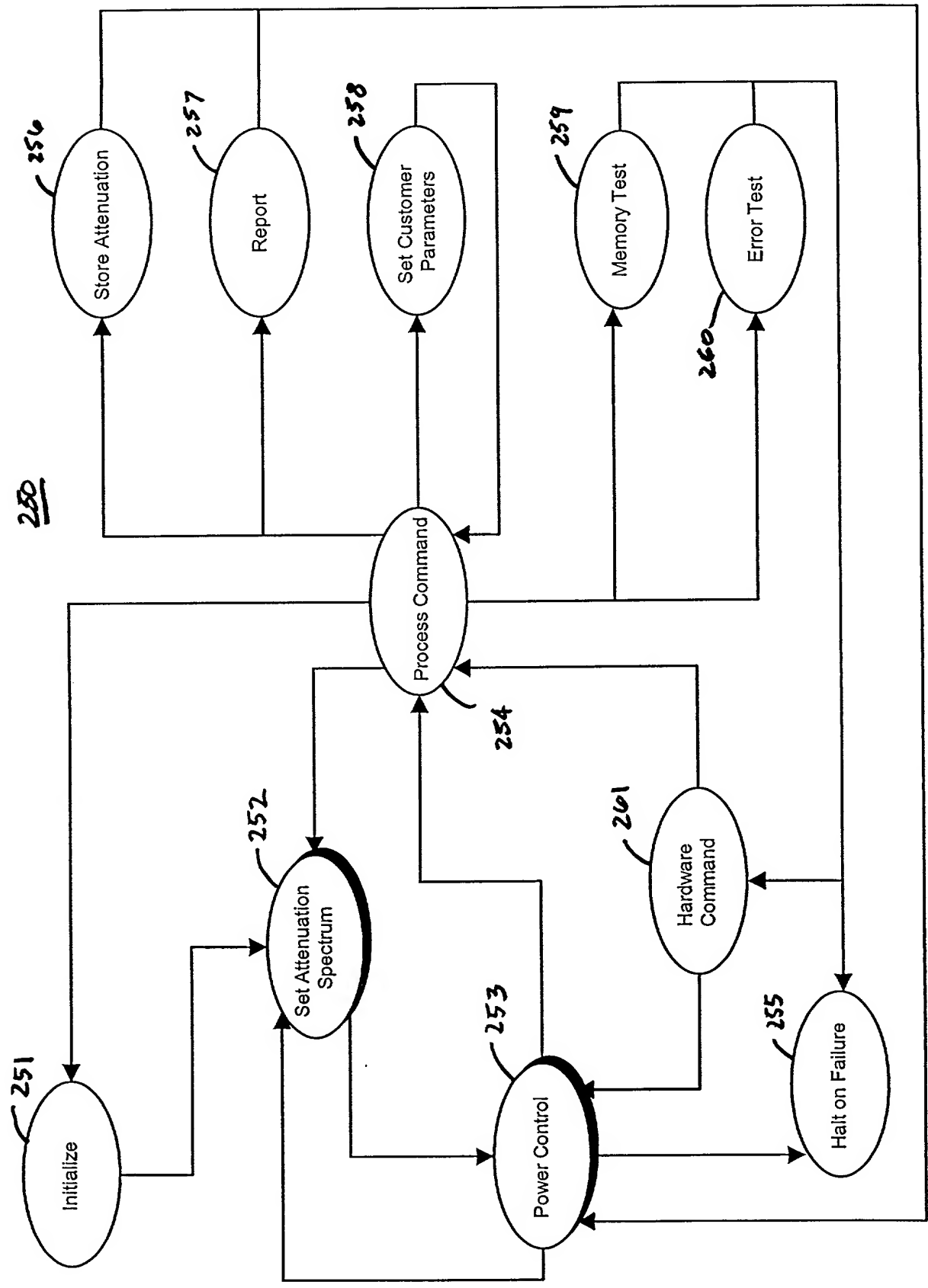
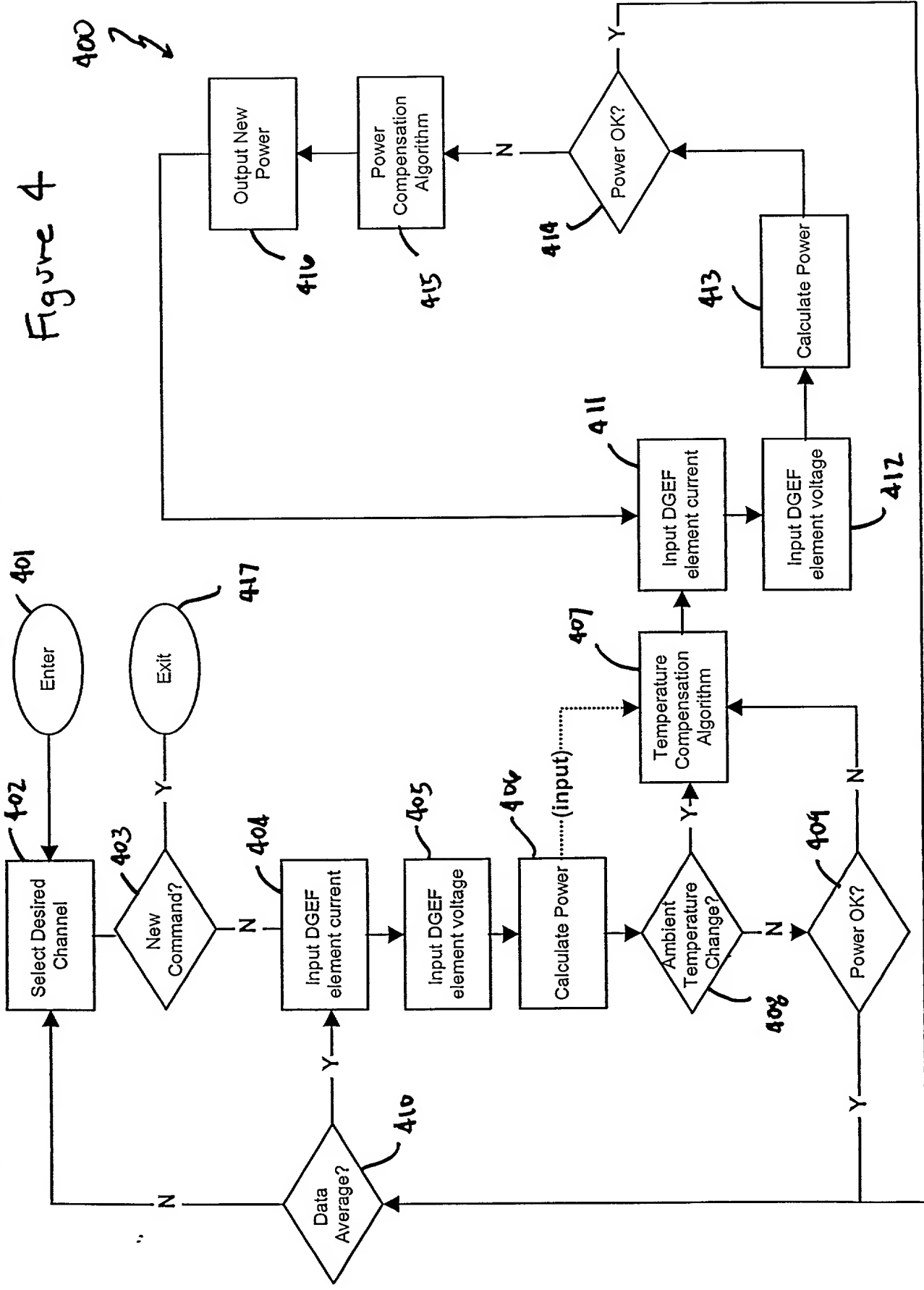


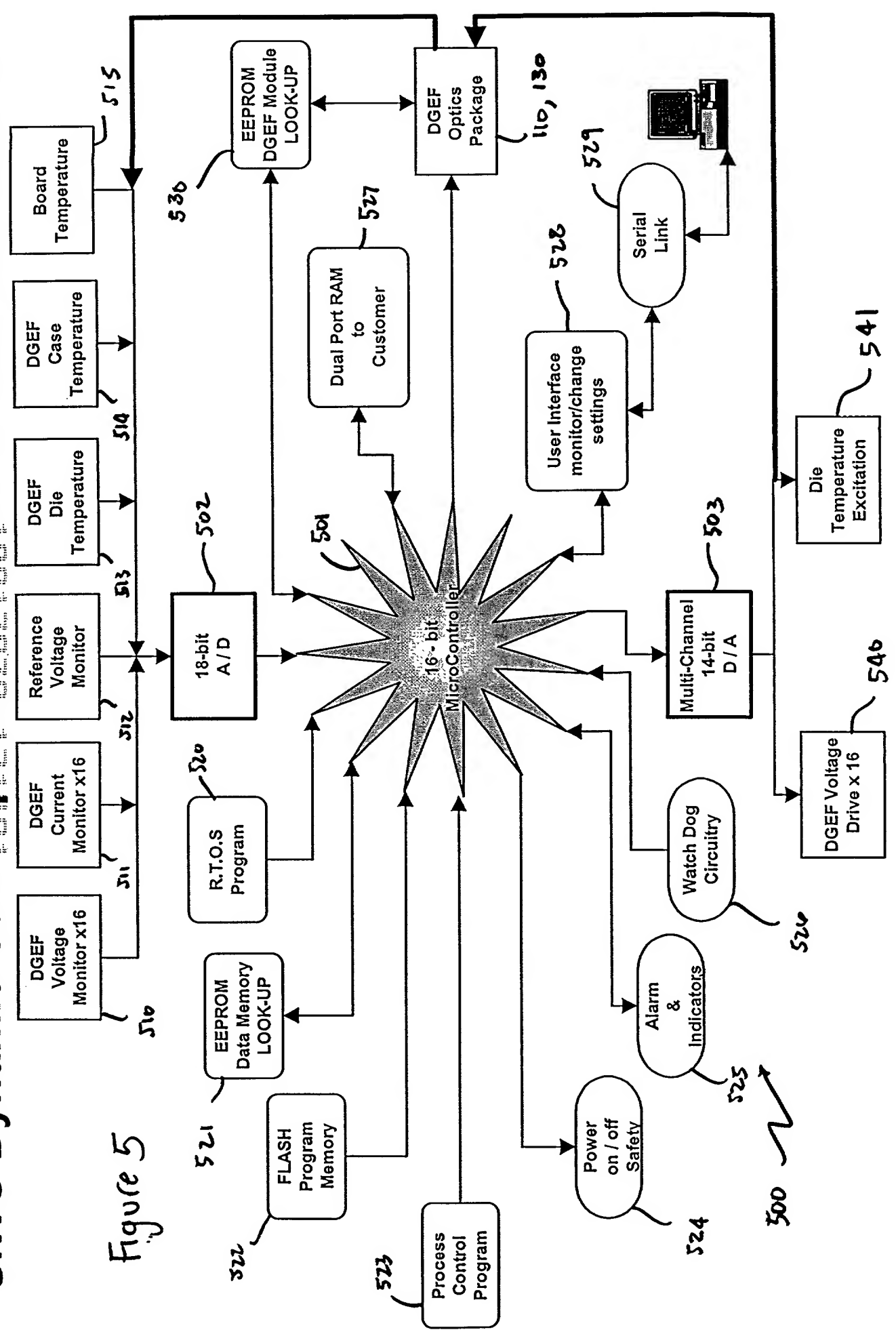
Figure 3

Dynamic Gain Equalization Filter Power Control State



SiWG Dynamic Gain Equalization Filter Embedded Control

Figure 5



Dynamic Gain Equalization Filter Firmware Architecture

Figure 6

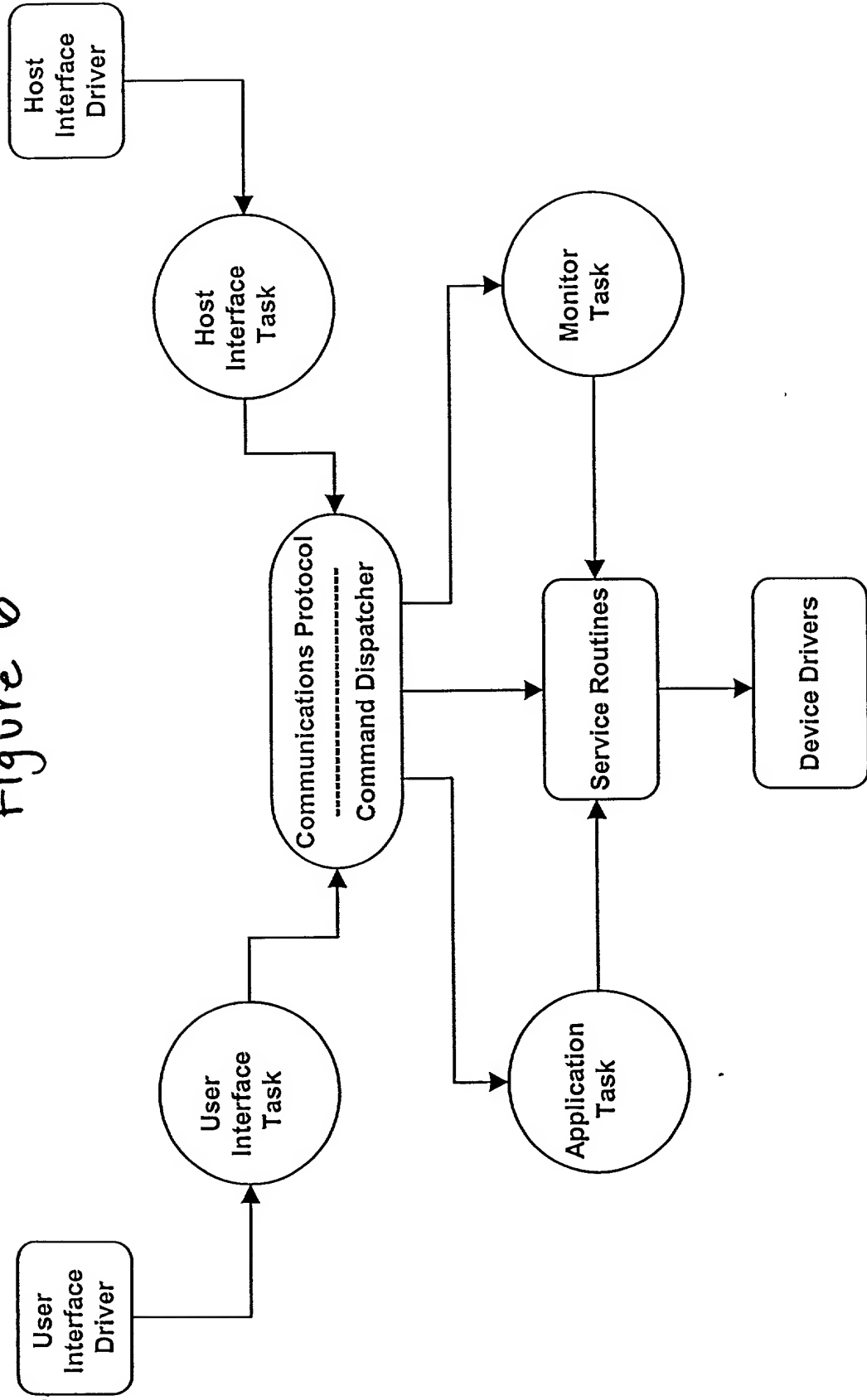


Figure 7

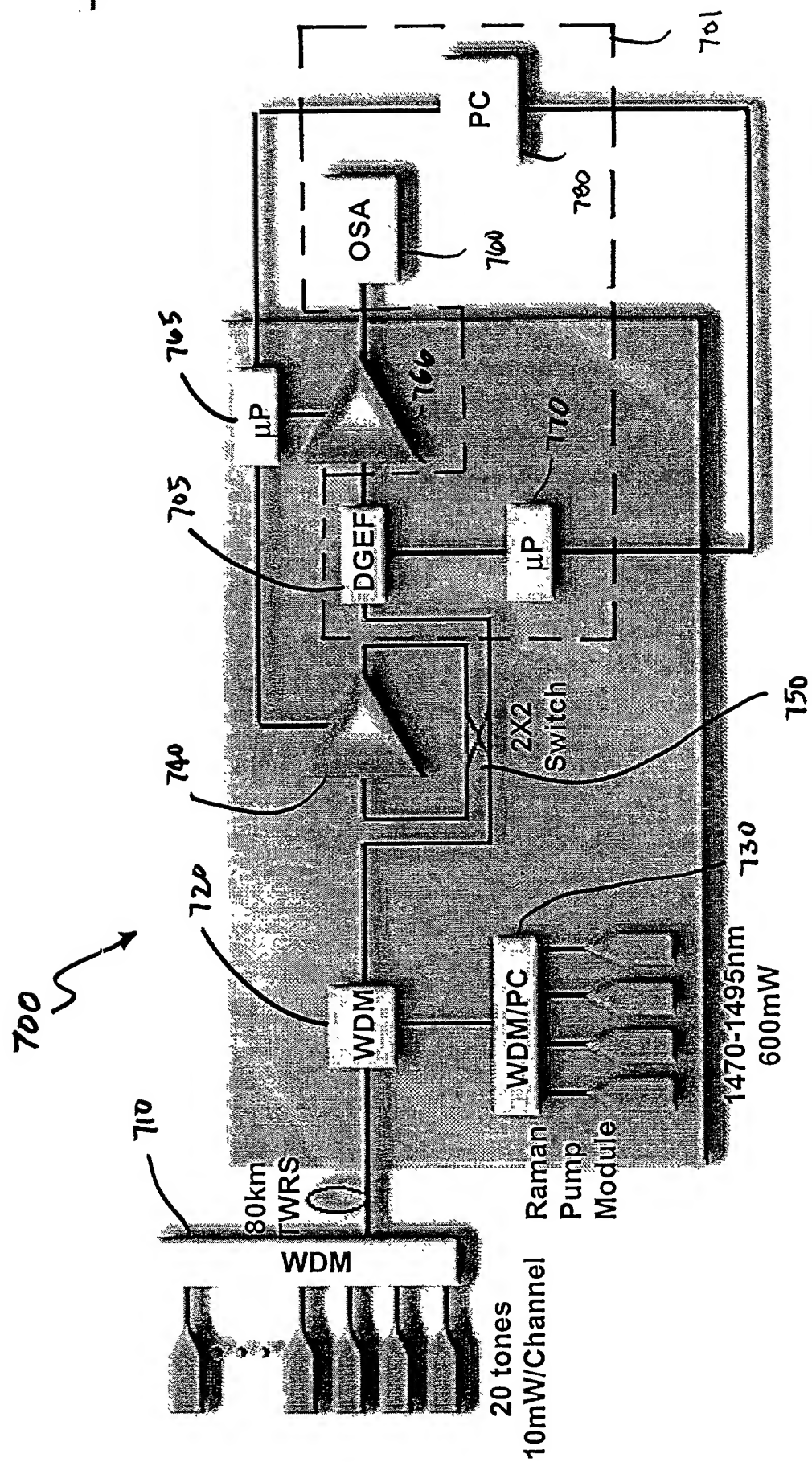


Figure 8

